

## About The Conference

3rd International Conference on Distributed Computing and Electrical Circuits and Electronics (ICDCECE-2024) aims at bringing together researchers, scientists, engineers, research scholars and industrial participants around the world to a common platform to share their experiences, new ideas, and research findings on all aspects of Computer Engineering and Electrical and Electronics Engineering. The researchers will present the state of the art of expansion and technical explanation covering a wide range of engineering subjects. This conference offers an international forum for sharing ideas, discussing the findings of research and introducing theoretical and practical applications in these fields. The current focus of global science, Electronics & Communication Engineering trends, interdisciplinary research and engineering activities are expressed in this conference. Authors are solicited to contribute to the conference by submitting articles that illustrate research results, projects, experiment works and industrial experiences that describe significant advances in the following areas, but are not limited to:

## Conference Tracks

- ☑ 5G and 6G
- ☑ Applied computing
- ☑ Artificial intelligence & Machine learning
- ☑ Big Data
- ☑ Bio-engineering, Bioinformatics & Bio-medical Engineering
- ☑ Cloud Computing & Cluster Computing
- ☑ Computer architecture & security
- ☑ Computer vision
- ☑ Data Intensive Computing Architecture
- ☑ Data mining & Data Stream Mining
- ☑ Electric Vehicle
- ☑ Evolutionary computing, Nature inspired computing
- ☑ Game theory & Graph theory
- ☑ Grid computing & Green Computing
- ☑ High Performance Adaptive and Evolutionary Computing
- ☑ Human-computer interaction
- ☑ Internet of Things (IOT)
- ☑ Knowledge Management and Networks
- ☑ Machine Learning
- ☑ Mobile Computing
- ☑ Neural Networks
- ☑ Pattern Recognition
- ☑ Power system and Electronics
- ☑ Self-organizing learning methods
- ☑ Sensor Networks and Social Sensing
- ☑ Soft Computing
- ☑ Software Engineering
- ☑ System Theory and Control Theory
- ☑ Video and Image Processing
- ☑ Wireless Networks Management
- ☑ Wireless Protocols and Architectures

Submission Deadline	15 <sup>th</sup> January, 2024
Notification of Acceptance	20 <sup>th</sup> February, 2024
Registration Deadline	15 <sup>th</sup> March, 2024
Conference	26 <sup>th</sup> & 27th April, 2024

Basavarajeswari Group of Institutions

## Ballari Institute of Technology & Management



Autonomous Institute under VTU, Belagavi  
( A unit of T.E.H.R.D. Trust © An ISO 9001:2015 Certified Institution)

“ Jnana Gangotri ” Campus, Ballari-Hosapete Road  
Near Allipura, Ballari - 583104 ( Karnataka)



In Association with



## 3<sup>rd</sup> IEEE International Conference

# ICDCECE - 2024

26<sup>th</sup> - 27<sup>th</sup> April 2024

Ballari | Karnataka | India



3<sup>rd</sup> IEEE International Conference on  
Distributed Computing and Electrical Circuits and Electronics  
(ICDCECE-2024)

## About the Institution

With its forte being accomplished, highly proficient and devoted faculty, with a sound knowledge of the subject, along with an innate desire to go toe to toe with the times and latest developments in emerging fields, has been BITM, Ballari Institute of Technology and Management's greatest strength. The institute's USP is its unfathomable student-teacher rapport and a student study itinerary to give shape to the student's overall outlook. Preparing students for meeting futuristic challenges of the morrow are monitored very closely by the faculty, with a personal touch. Feedbacks, after counseling students and parents, to arrive at solutions play a very important role in seeing students achieve their zenith. To take the big leap in the corporate milieu, in house training and projects, soft skills and R&D are subjects essential to the student curriculum. Workshops and personality Developments, Industry Institution Interactions are routine on campus.

The Governing body of BITM has shown its mettle, as its members are pooled in from various professions such as academicians from IIM, IITs, Industrialists, Medical & Software professionals and Chartered Accountants.

A dedicated internet connectivity along with high speed wireless networking together with latest computers and software albeit with well-equipped laboratories and a spacious library, rich in collection of books and journals speaks volumes about BITM's state of the art infrastructure.

## Call for Papers

Prospective authors are encouraged to submit high quality original, previously unpublished, experimental or theoretical research papers for presentation at the Conference. Articles submitted to the conference should meet these criteria and must not be under consideration for publication elsewhere. The paper should be in the format strictly as per the Paper Template given below. The papers should be drafted in English with a maximum of Eight (8) pages and minimum of 4 pages including figures, tables and references. The paper will be peer reviewed by domain experts of the respective tracks by the Technical Program Committee. Authors should submit the papers through Easy Chair. Kindly do not submit the paper multiple times; it may lead to cancellation of your paper.

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